



DOCKET NO: 217806US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

RE APPLICATION OF

:

TAKASHI MIURA, ET AL.

: EXAMINER: BONZO, B.

SERIAL NO: 10/028,804

:

FILED: DECEMBER 28, 2001

: GROUP ART UNIT: 2114

FOR: SEMICONDUCTOR INTEGRATED
CIRCUIT, SYSTEM BOARD AND
DEBUGGING SYSTEM

:

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated September 7, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.